

Figure 1

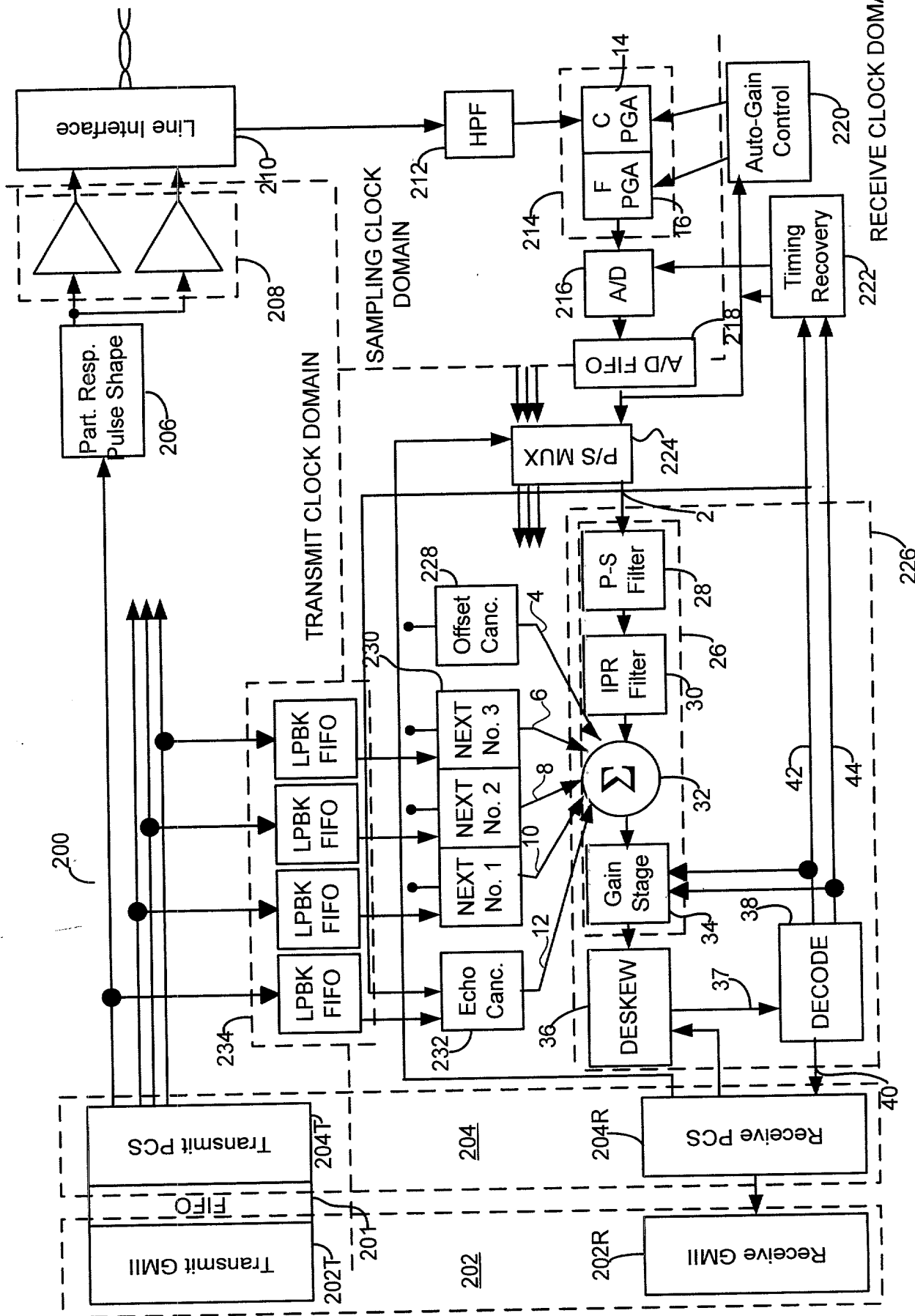


Figure 2

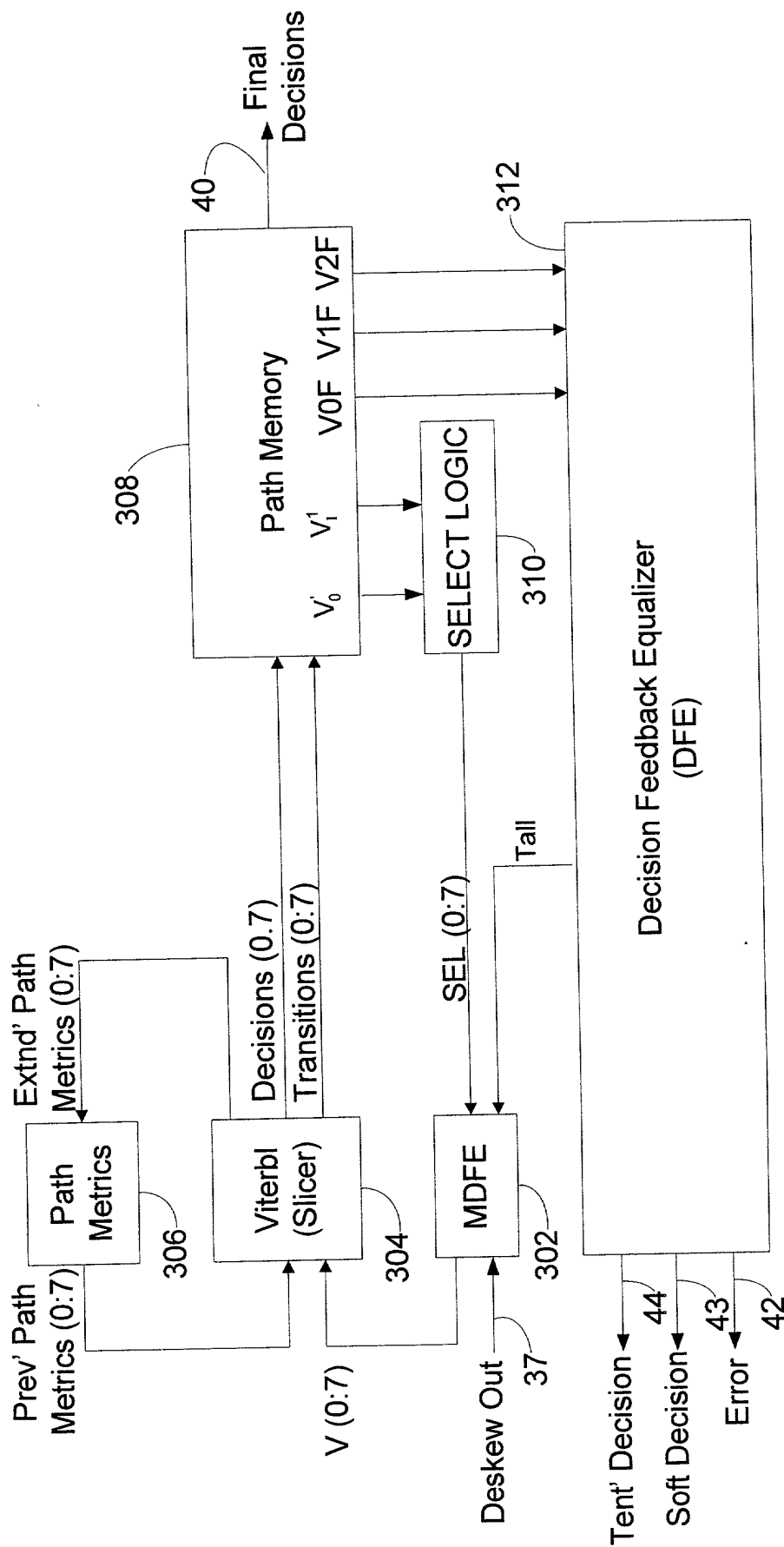


Figure 3

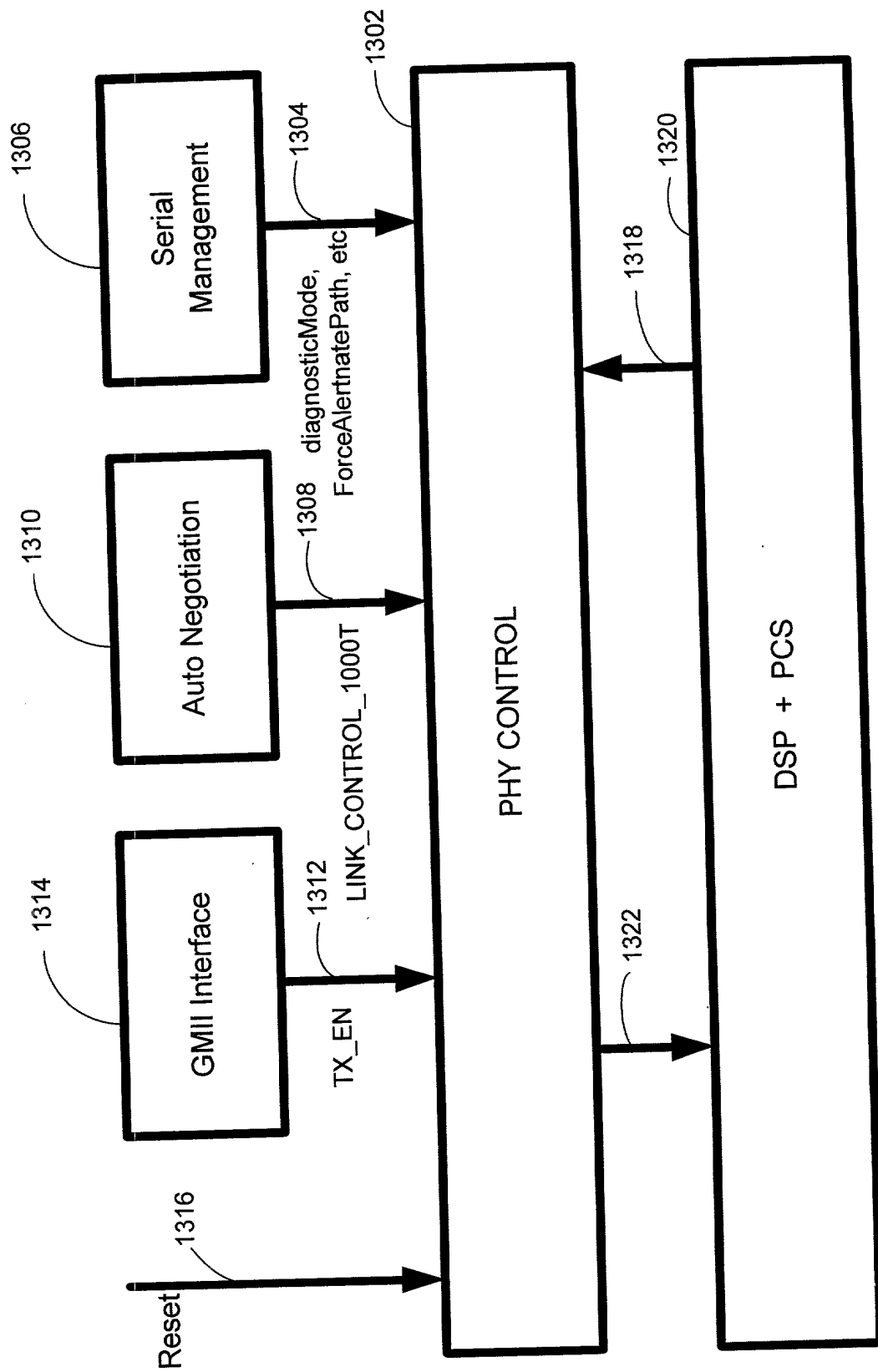


FIG. 4

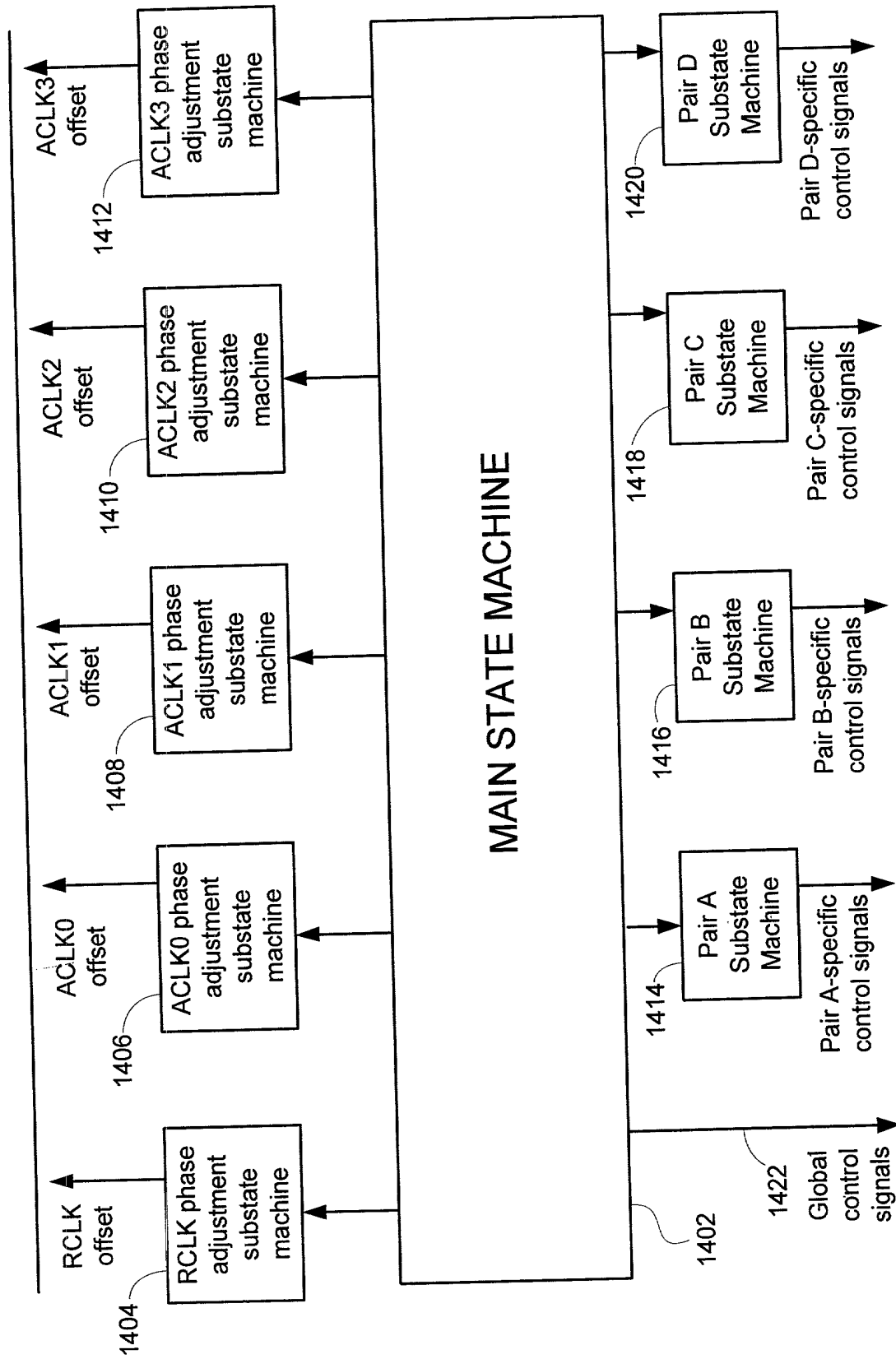


Figure 5

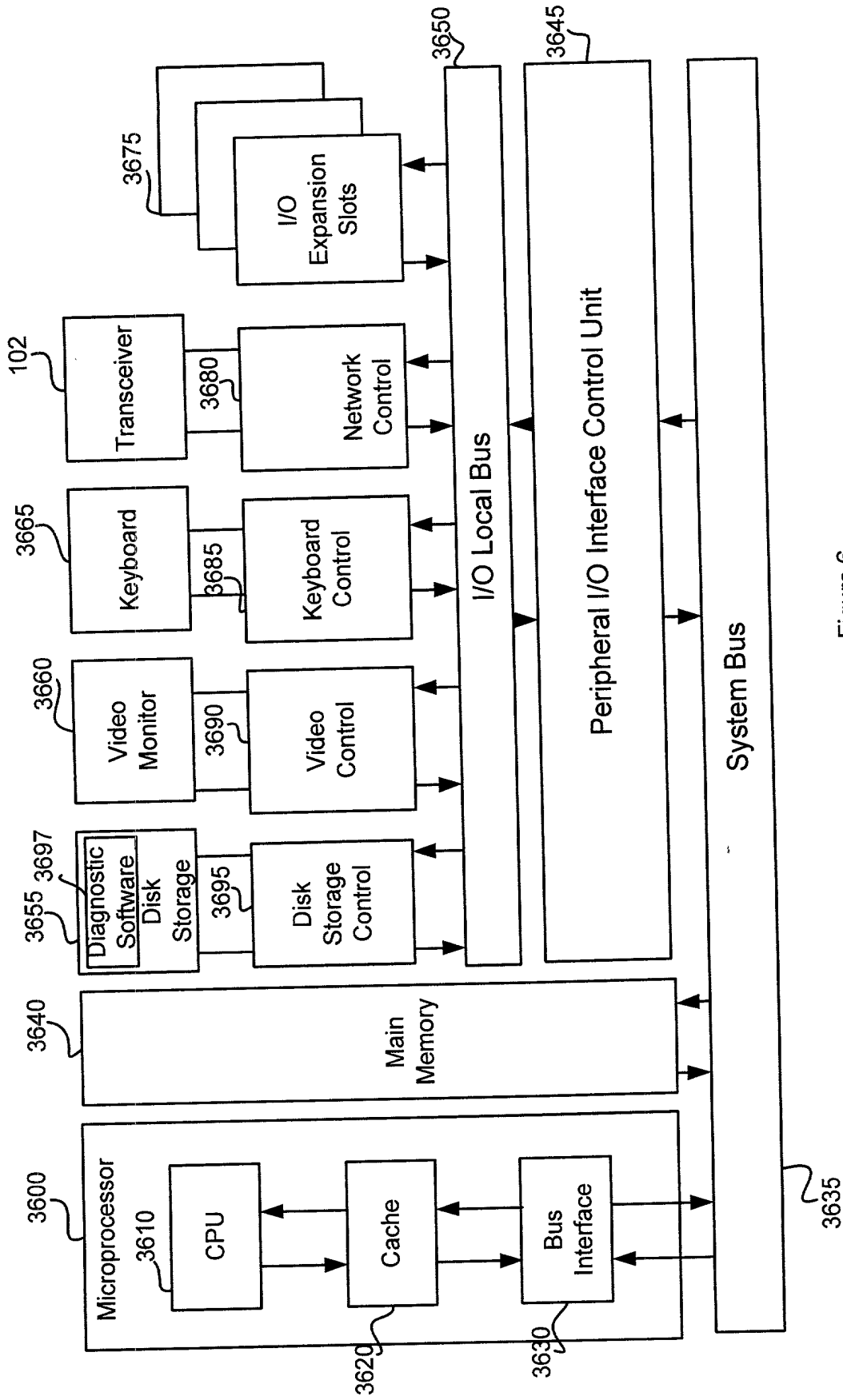


Figure 6

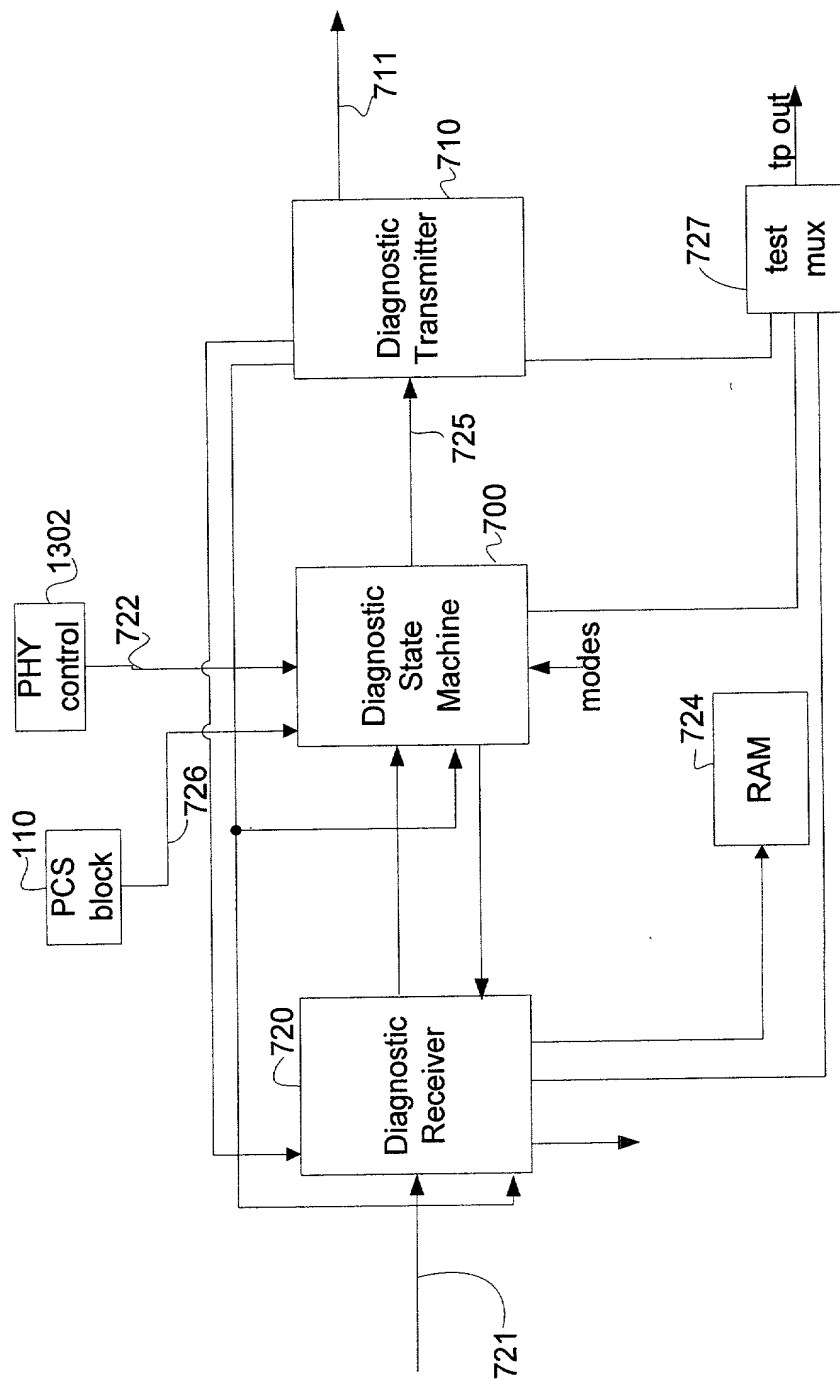


Figure 7

Master - no FEXT

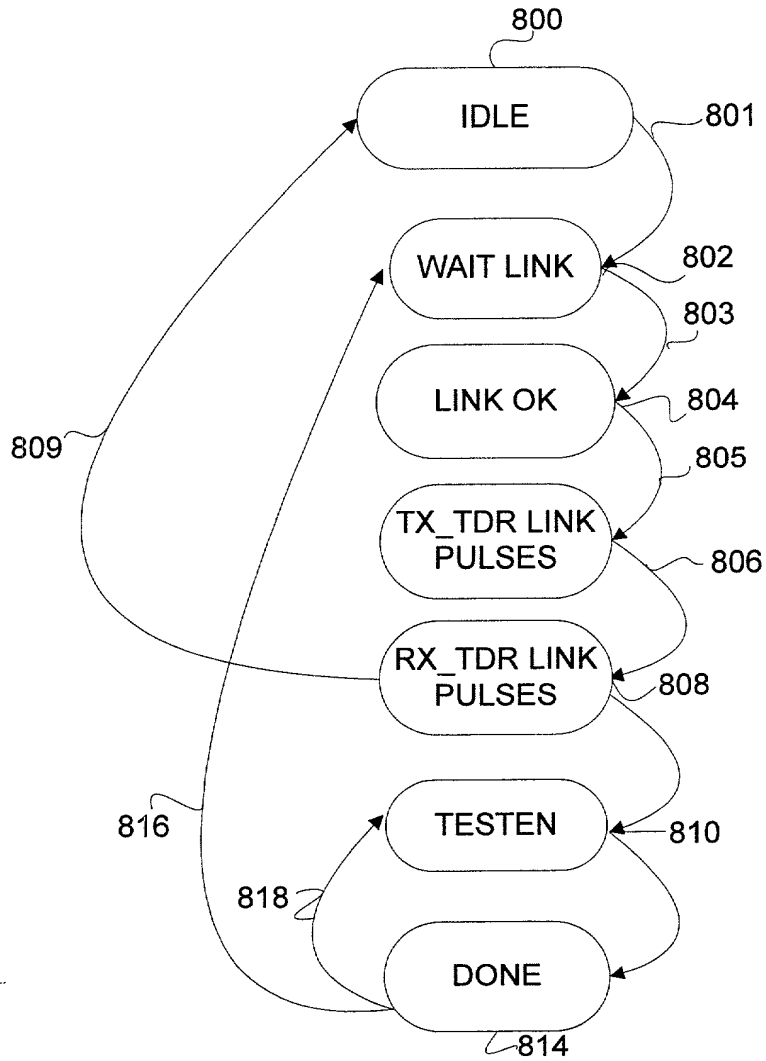


Figure 8

slave - no FEXT

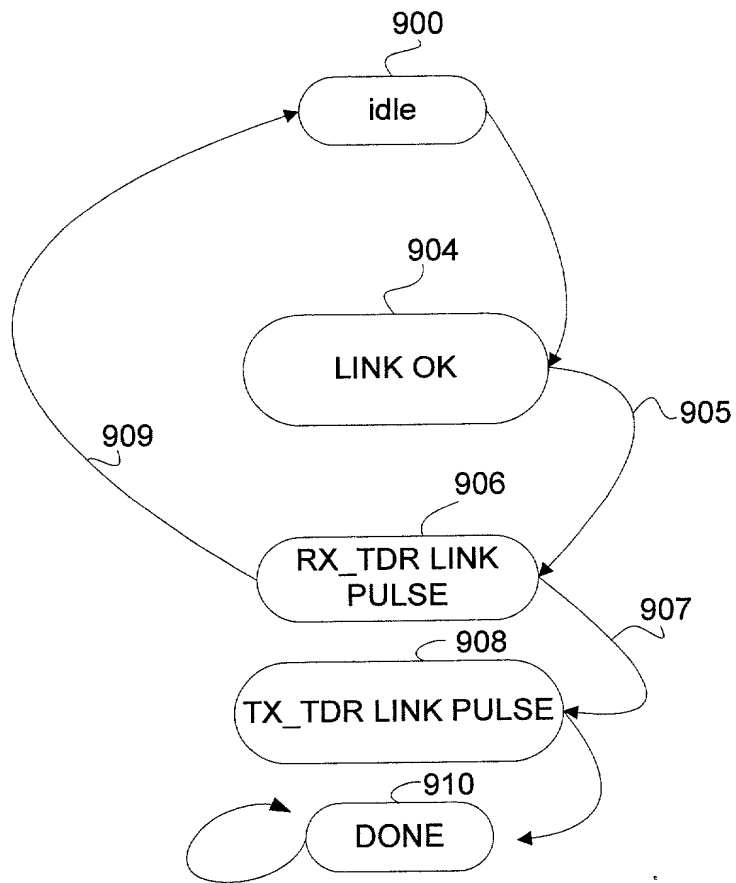


Figure 9

Echo/Next - no extphy

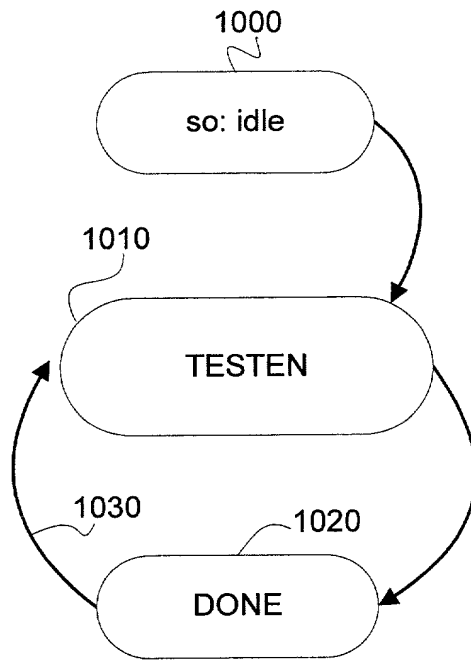


Figure 10

Master - FEXT

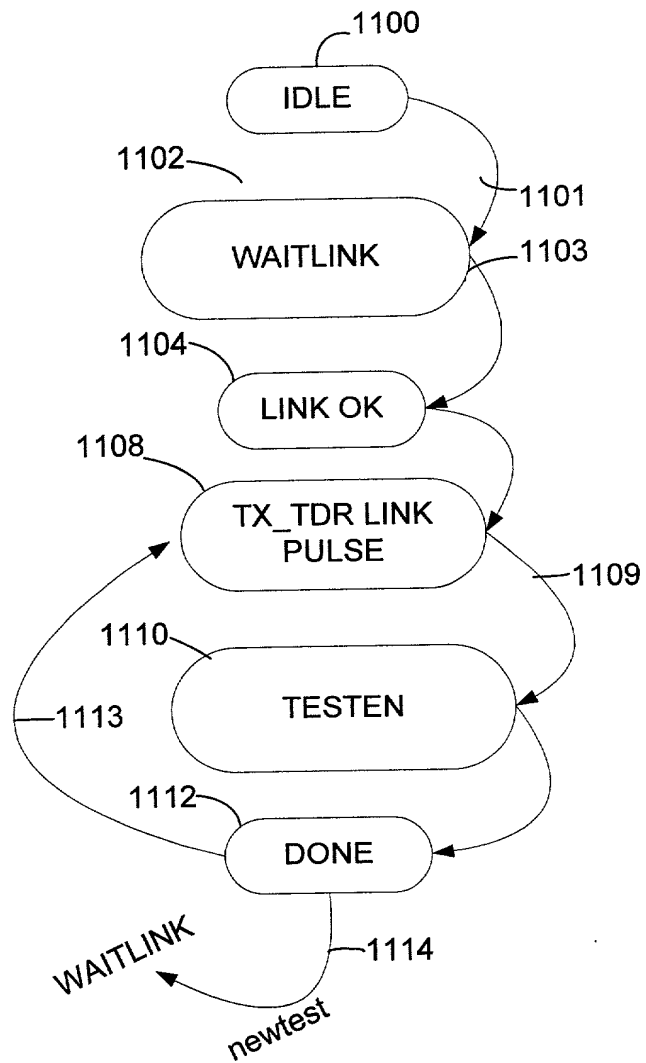


Figure 11

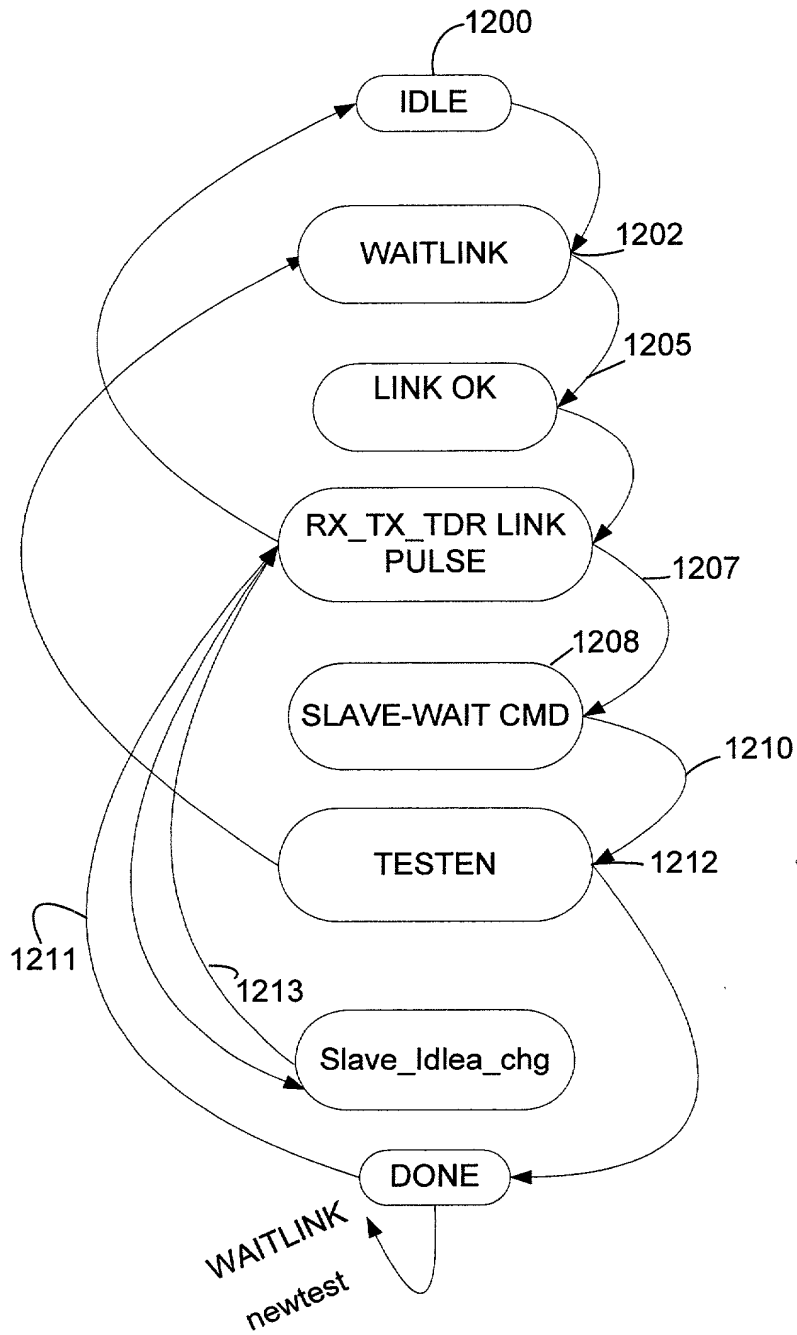


Figure 12

linkdet fsm

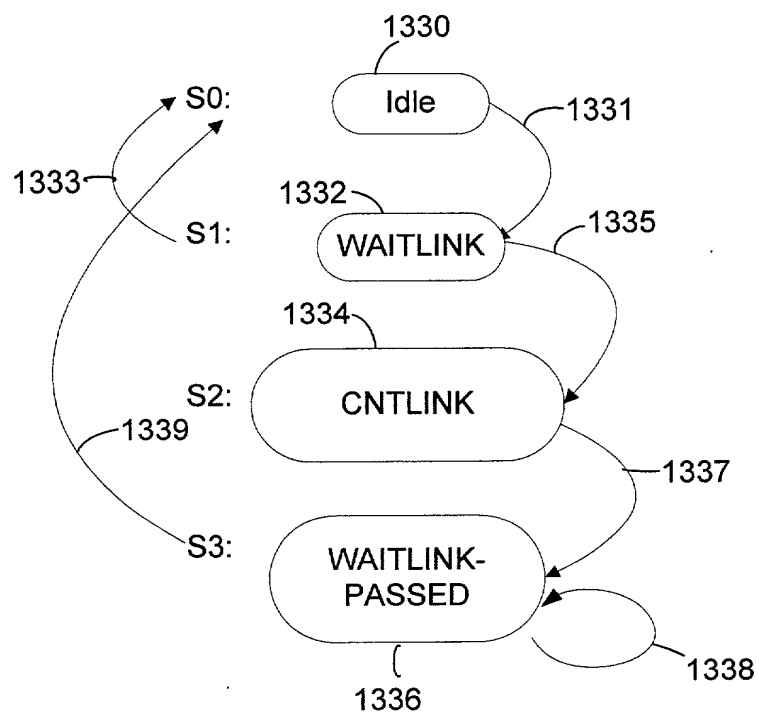


Figure 13

LINKPULSE/TEST PULSE TIMING - NO FEXT

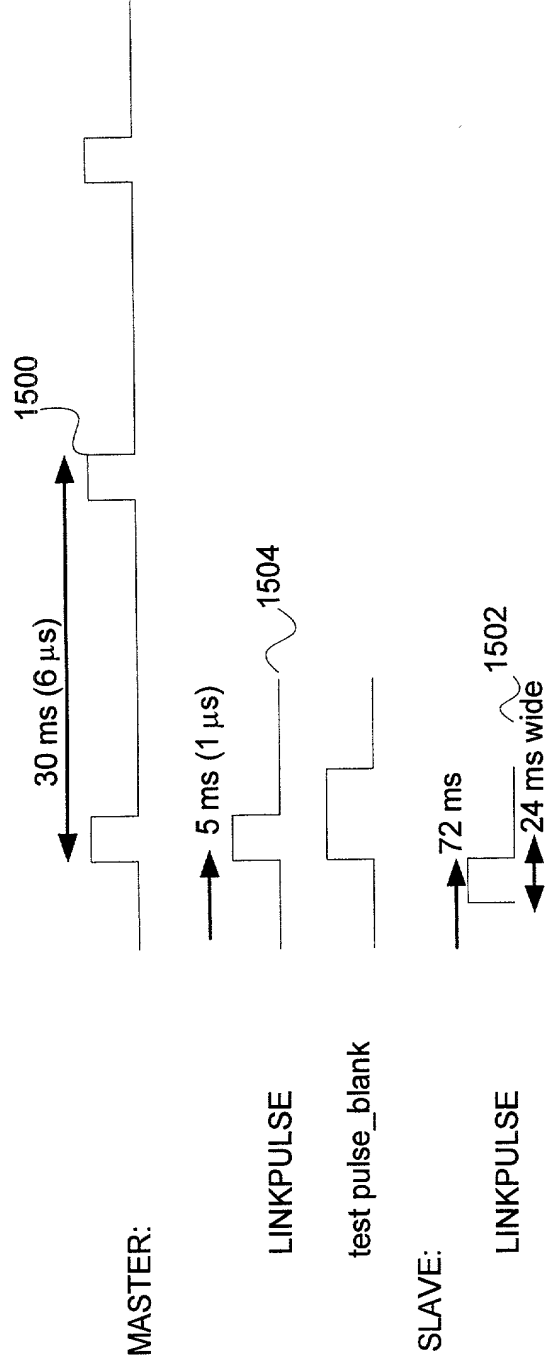


Figure 15

MDIX: Link pulse - no fext

1) No mdix

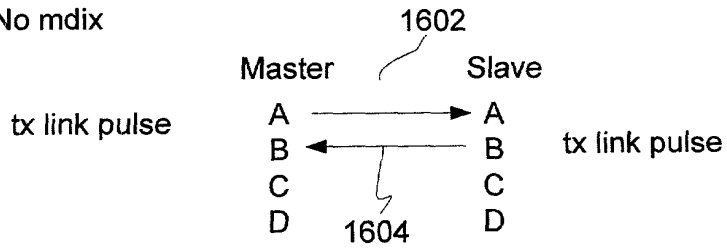


Figure 16A

2) mdix

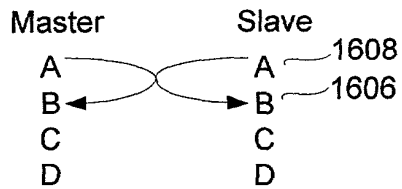
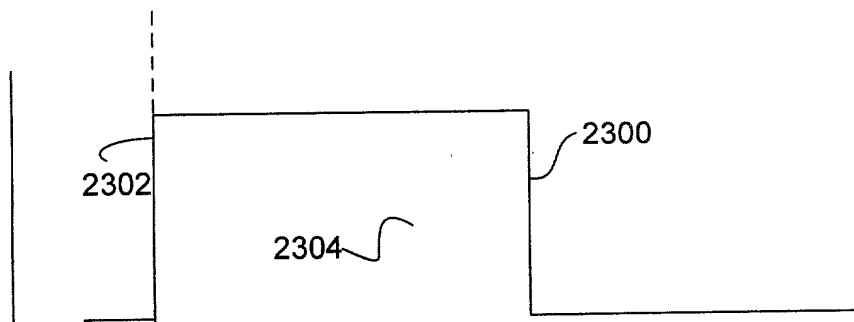


Figure 16B

Test
Signal



Data
Signal

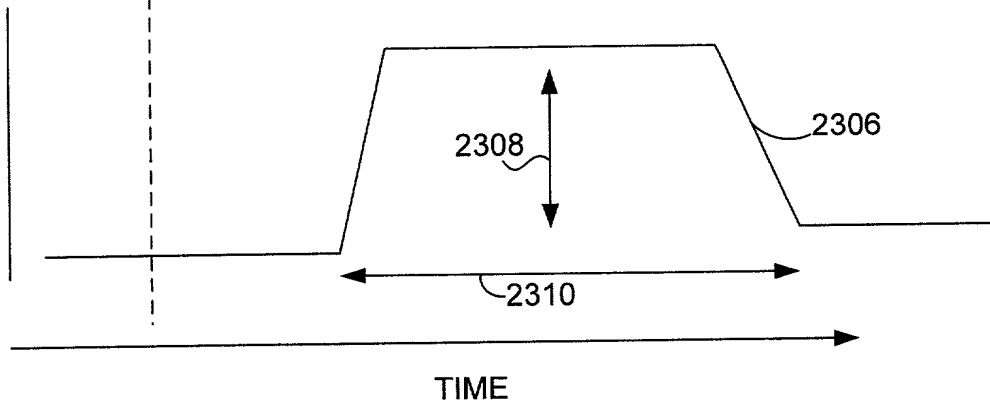


Figure 17

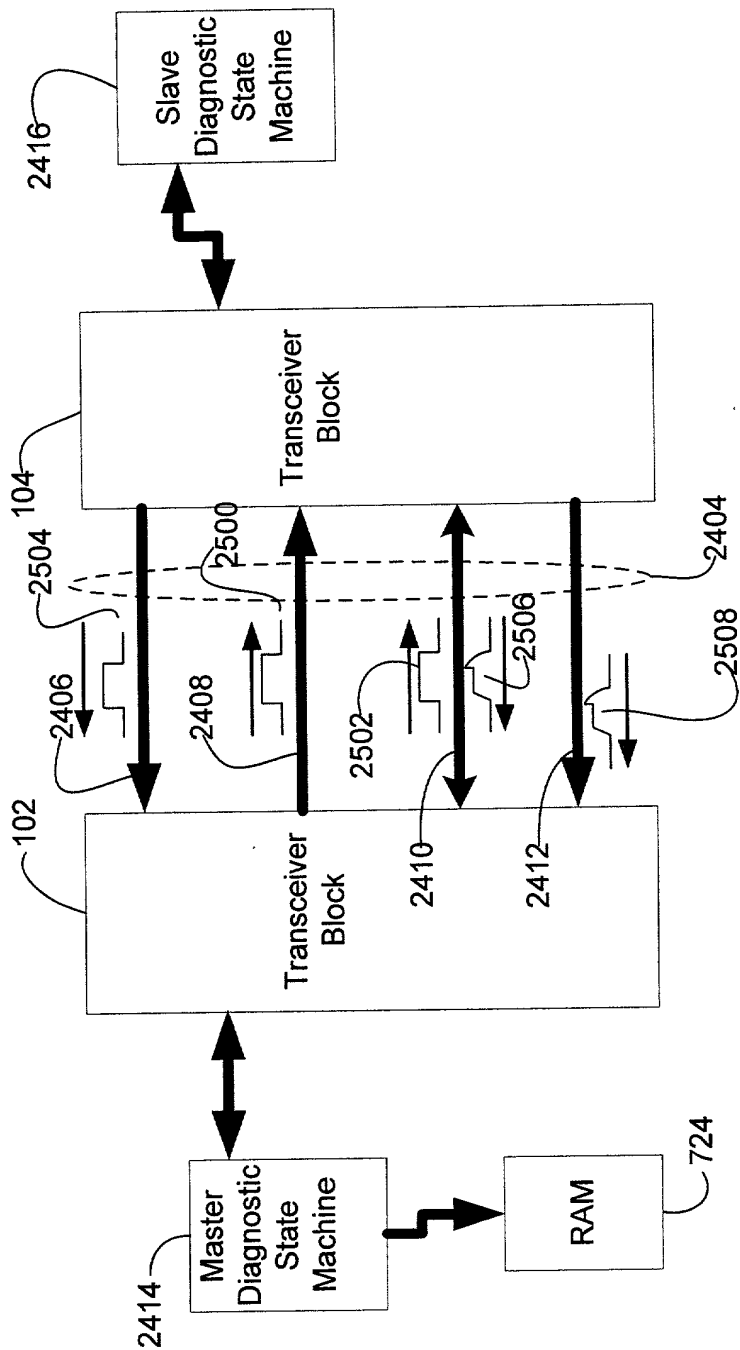


Figure 19

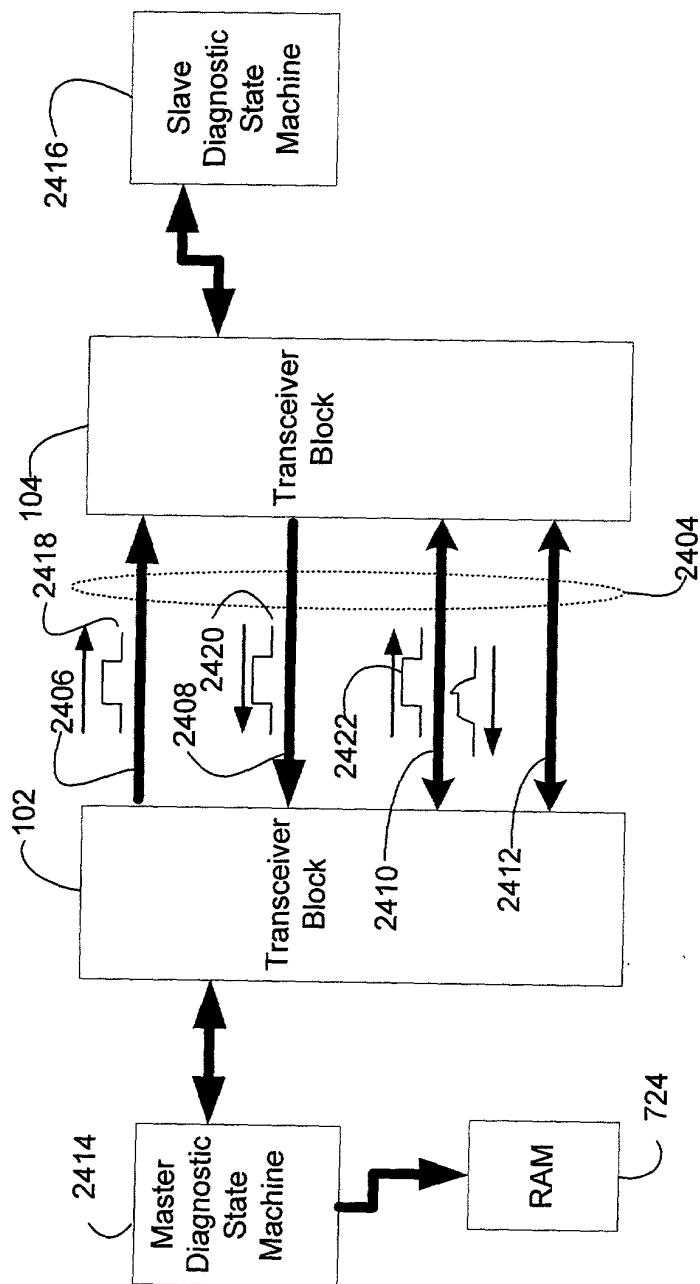


Figure 20